

PRIOR ART FIG.1

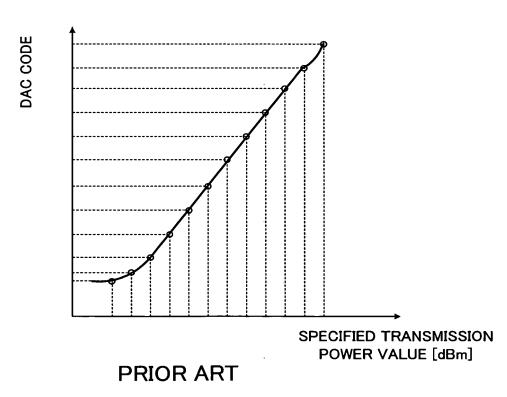
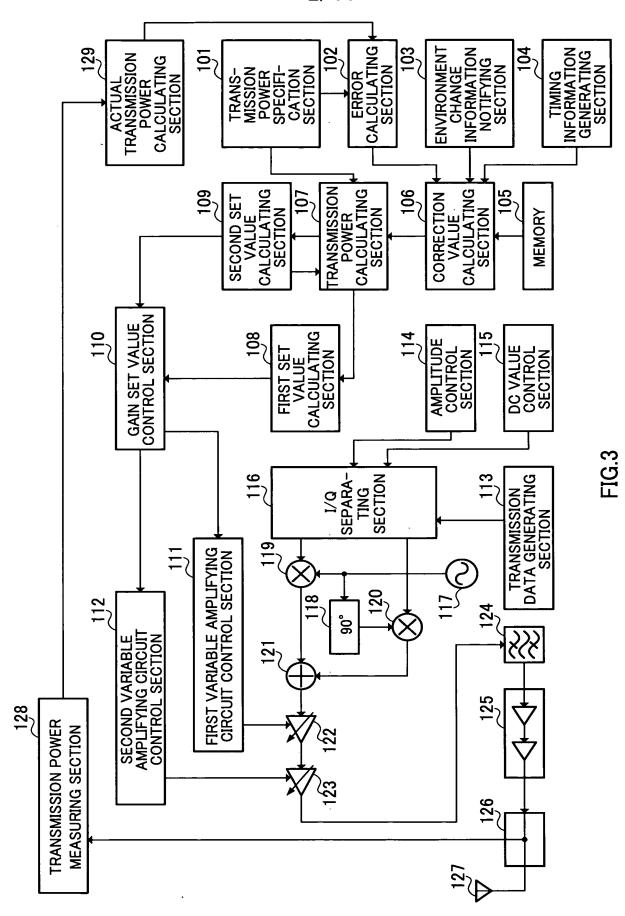


FIG.2



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GAIN SET FOR FIRST VARIABLE AMPLIFYING CIRCUIT	GAIN IN FIRST VARIABLE AMPLIFYING CIRCUIT
0[dB]	x[dB]
1[dB]	x+1[dB]
2[dB]	x+2[dB]
3[dB]	x+3[dB]
4[dB]	x+4[dB]
5[dB]	x+5[dB]
•	•
n−1[dB]	x+(n-1)[dB]
n[dB]	x+n[dB]

FIG.4

GAIN SET FOR SECOND VARIABLE AMPLIFYING CIRCUIT	GAIN IN SECOND VARIABLE AMPLIFYING CIRCUIT
−0.1*m[dB]	−0.1*m[dB]
-0.1*(m-1)[dB]	-0.1*(m-1)[dB]
•	•
−0.2[dB]	−0.2[dB]
−0.1[dB]	−0.1[dB]
0[dB]	0[dB]
0.1[dB]	0.1[dB]
0.2[dB]	0.2[dB]
•	•
0.1*(m−1)[dB]	0.1*(m−1)[dB]
0.1*m[dB]	0.1*m[dB]

FIG.5

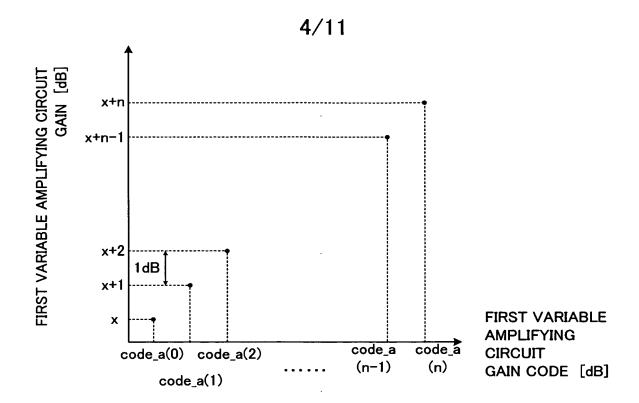


FIG.6

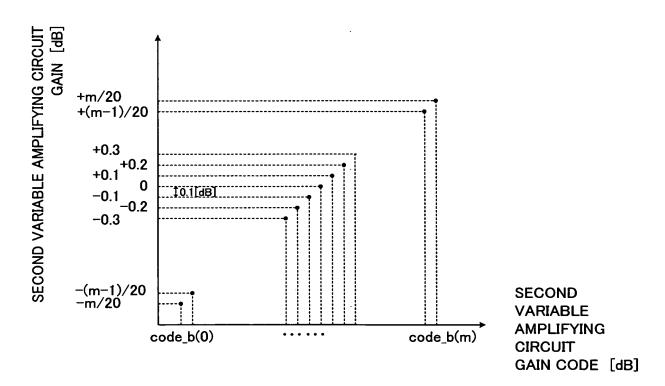


FIG.7

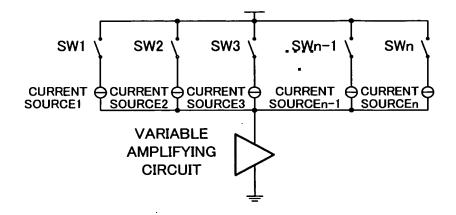


FIG.8

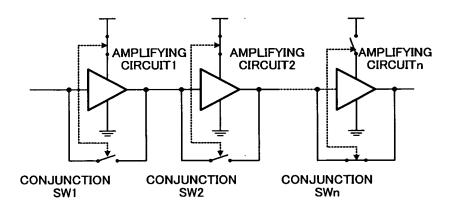


FIG.9

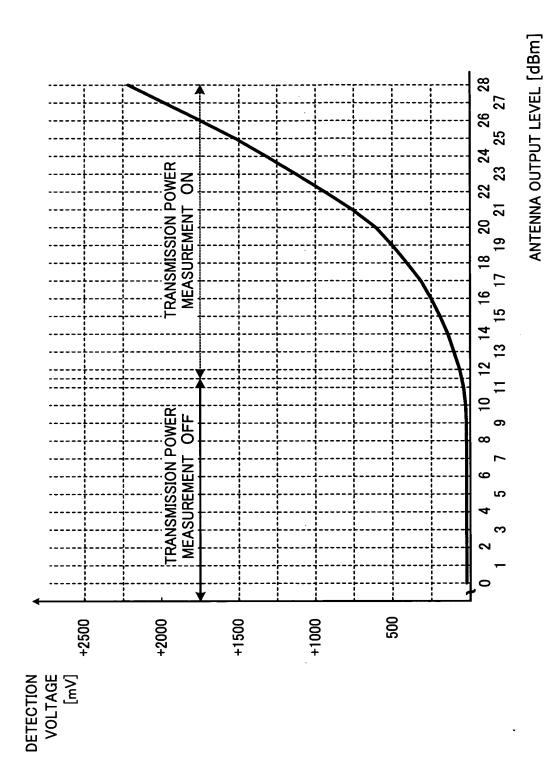
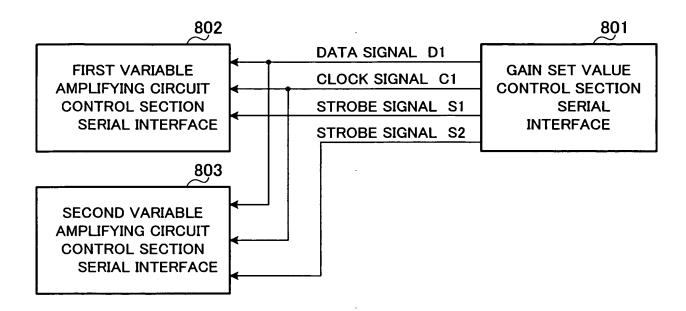


FIG. 10

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**FIG.11** 

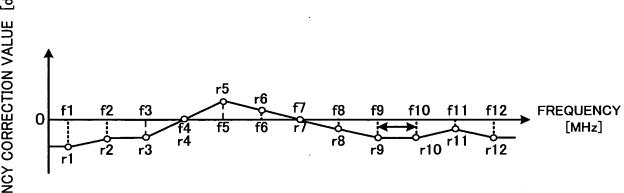
MSB (Most Significant Bit)		LSB (Least Significant Bit)
	VGA1 GAIN SET DATA	VGA1 RESISTOR ADDRESS
MSB (Most Significant Bit)  LSB (Least Significant Bit)		
	VGA2 GAIN SET DATA	VGA2 RESISTOR ADDRESS

**FIG.12** 

MSB (Most Significant Bit	E)	LSB (Least Significant Bit)
VGA2 GAIN SET DATA	VGA1 GAIN SET DATA	RESISTOR ADDRESS

SPECIFIED TRANSMISSION POWER VALUE [dBm]	VARIABLE AMPLIFYING CIRCUIT SET VALUE [dB]
-56[dBm]	(p−80)+0.1*q[dB]
•	•
−3[dBm]	(p−27)+0.1*q[dB]
-2[dBm]	(p-26)+0.1*q[dB]
-1[dBm]	(p-25)+0.1*q[dB]
0[dBm]	(p-24)+0.1*q[dB]
•	•
+23[dBm]	(p-1)+0.1*q[dB]
+24[dBm]	p+0.1*q[dB]

FIG.14



FREQUENCY CORRECTION VALUE [dB]

FREQUENCY [MHz]	FREQUENCY CORRECTION VALUE [dB]
f1	r1[dB]
f2	r2[dB]
f3	r3[dB]
f4	r4[dB]
f5	r5[dB]
:	•
f11	r11[dB]
f12	r12[dB]

FIG.16

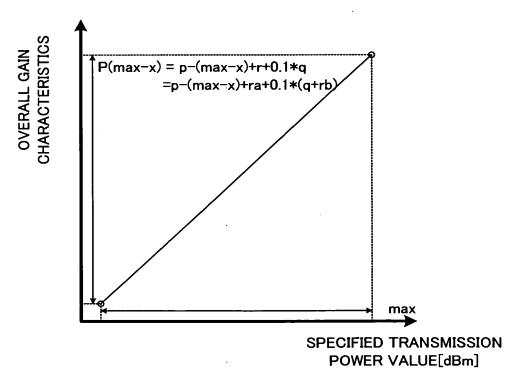


FIG.17

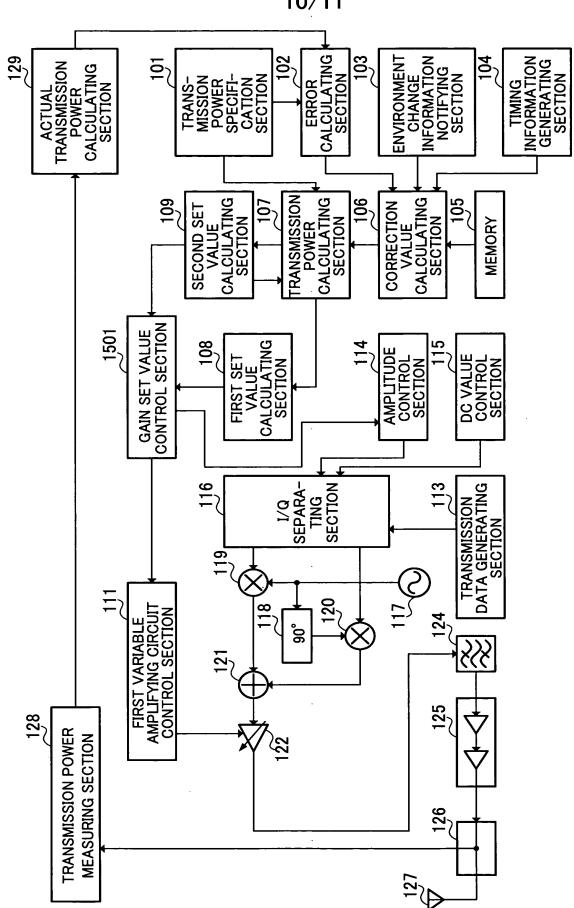


FIG. 18



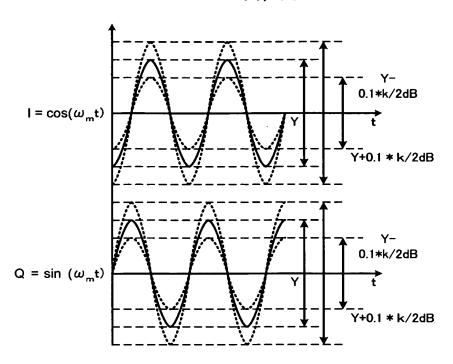


FIG.19

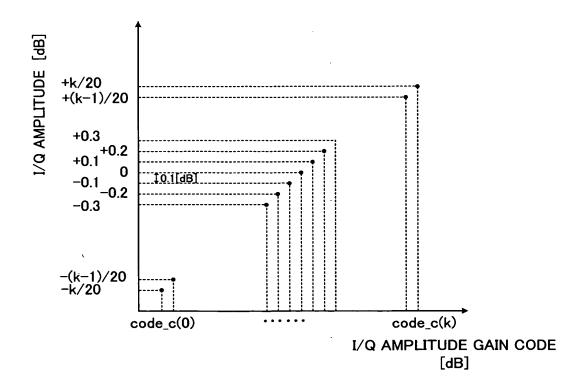


FIG.20